

PATENT 81788.0026

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Seiichi MORI 🗸

Serial No: 09/456,873

Filed: December 8, 1999

Non-Volatile Semiconductor Memory For:

Device ~

2824 Art Unit:

Examiner: M. Carter

United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents

Washington D.C. 20231, on September 27, 2000

Date of Deposit <u>óuis Al Mo</u>k

signature

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## TRANSMITTAL OF INFORMATION DISCLOSURE **STATEMENT**

**Assistant Commissioner for Patents** Washington, D.C. 20231

Dear Sir:

The information disclosure statement submitted herewith is being filed before the first action on the merits of this application.

If it should be determined that for any reason either an insufficient or excessive fee has been paid, please charge any insufficiency or credit any overpayment necessary to ensure consideration of the information disclosure statement for the above-identified application to Deposit Account No. 50-1314. A copy of this paper is enclosed.

By:

Respectfully submitted,

HOGAN & HARTSON L.L.P.

Date: September 27, 2000

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